

Attorney's Docket No.: 002282.P055

Patent

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:

Raynaud, et. al.

Application No.: 09/127,584

Filed: July 31, 1998

For: METHOD AND APPARATUS FOR GATE-
LEVEL SIMULATION OF SYNTHESIZED
REGISTER TRANSFER LEVEL DESIGNS
WITH SOURCE-LEVEL DEBUGGING



Examiner: Sharmalla Simpson

Art Unit: 2764

I hereby certify that this correspondence is being deposited
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4/21/99

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Assistant Commissioner for Patents
Washington, D.C. 20231

SUBMISSION OF SUPPLEMENTAL DECLARATION
PURSUANT TO 37 C.F.R. § 1.67 TO CORRECT AN ERROR

Sir:

Enclosed herewith for filing in the above-referenced patent application is a
Supplemental Declaration submitted pursuant to 37 C.F.R. § 1.67. This Declaration is being
submitted to correct an error in the previously filed Declaration, which erroneously omitted a
sentence regarding willful false statements and truthfulness.

If any fee is required, please charge Deposit Account No. 02-2666.

Respectfully submitted,

BLAKELY, SOKOLOFF, TAYLOR & ZAFMAN LLP

Dated: 4/21/99

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